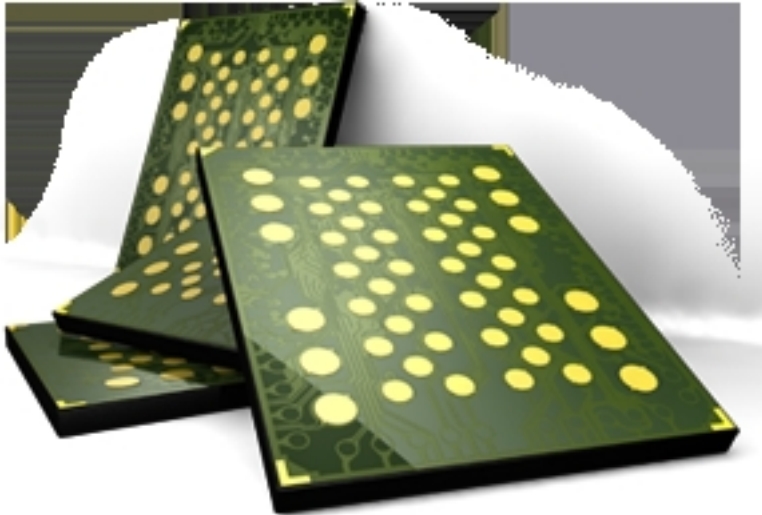**Micron Goes After NAND Memory Errors**

Written by Marco Attard
 09 December 2010

Micron integrates error management techniques with its NAND products with ClearNAND.

As semiconductor circuity shrinks, data error rates increase, calling for more sophisticated error correction code (ECC).

ClearNAND eliminates the ECC burden from the host processor through an application-specific integrated circuit (ASIC) and an 8 byte capacity NAND die acting a data collection cache.

The ClearNAND itself appears as a standard NAND interface to the host processor.

Micron uses its 25nm MLC process in its manufacture.

Products are available in 2 versions-- standard and enchanced.

Standard ClearNAND products are aimed at portable media players and other CE devices, in 8-32GB packages.

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Enchanced products add enterprise-specific features to the package. Available in 16-64GB versions, Enchanced ClearNAND target for enterprise and computing applications.

Go [Micron Unveils ClearNAND](#)