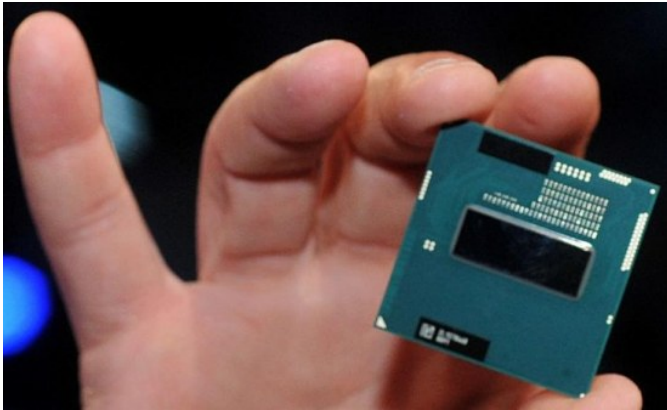


## More Low-Power Haswells from Intel

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24 July 2013

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Just a month after [the Computex 2013 launch](#) Intel teases more details on the 4th generation of Core processors, aka "Haswell"-- and promises a version operating at a scenario design point (SDP) of all of 4.5W.



It joins a version offering 6W SDP operation, both designed for use in thinner, fanless hybrid and 2-in-1 devices. The company does not provide further specifications on the latest Haswell addition.

But what does "scenario design point" actually mean? In a somewhat misleading turn in PR speak, SDP means Intel limits processor performance to allow it to run comfortably (at most times) in a chassis able to cool 4.5W heat.

The chip can run faster through more demanding tasks, but only for short periods as the temperature allows.

We will learn whether such marketing speak means anything once the first Haswell CPUs hit the market sometime during H2 2013.

Go [New 4th Generation Intel Core Processor](#)